


## Profile

<b>Name of the Faculty</b>	G.Ramachandra Kumar	
<b>Designation</b>	Assistant Professor	
<b>Department</b>	Electronics and Communication Engineering	
<b>Area of Interest</b>	VLSI	
<b>Subjects Taught</b>	Digital Design, Linear integrated Circuits, VLSI, CMC, MPMC	
<b>JNTUH Registration Id</b>	7112-150409-105014	
<b>College Staff Code</b>	SC1543	
<b>Official Mail</b>	Grckumar.ece@gcet.edu.in	

### Educational Qualifications:

S. No.	Degree	Specialization	University/College	Year
1	M.Tech	VLSI-SD	JNTUH	2014
2	B.Tech	IETE	AMITE	2011
3	Diploma	DEIE	SBTET	2002
4	SSC	SSC	State Board	1999

### Paper Publications:

S. No.	Publication details
1	Label image constrained multi atlas selection for image segmentation. IJECEC, IJECEC-V3-16-28, VOLUME :3,ISSUE:6,DEC-2017
2	Increasing IP Reuse ability by using OCP-AHB Wrapper for SOC integration. IJETTCS, ISSN-2278-6856, VOLUME :2,ISSUE:5,OCT-2013
3	"A Study on Various Approaches in Designing Ternary Half Adder," 2021 International Conference on Computer Communication and Informatics (ICCCI), 2021, pp. 1-9, doi: 10.1109/ICCCI50826.2021.9402582.
4	"Design and analysis of 32 bit high speed carry select adder" Journal of Physics:

	Conference Series, Volume 1916, 2021 International Conference on Computing, Communication, Electrical and Biomedical Systems (ICCCEBS) 2021 25-26 March 2021, Coimbatore, India.
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### **Books/Book Chapters Published:**

<b>S. No.</b>	<b>Publication details</b>
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### **Experience:**

<b>Teaching</b>	9 years
<b>Industry</b>	-
<b>Research</b>	-
<b>Total Experience</b>	9 years